

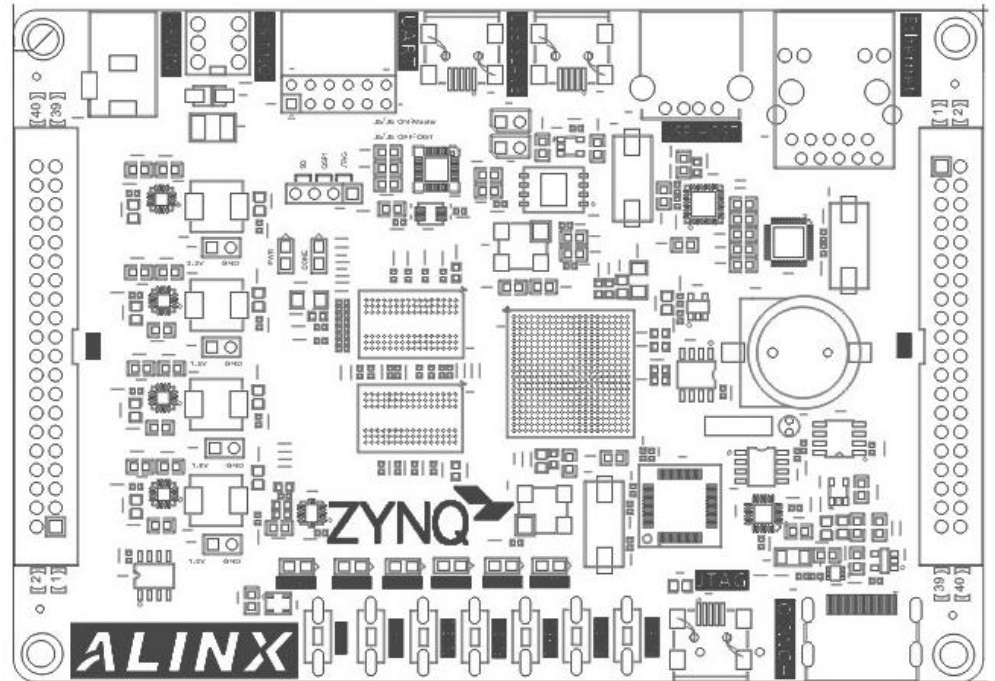


EENG 484 – Advanced Digital Design

XDC and FPGA fabric

ALINX AX7010 dev board

- a) Power input +5V ONLY!!!
- b) On/Off switch
- c) USB programming port
- d) Headers J10 and J11
- e) PMOD header
- f) Voltage regulators
- g) PL LED 1,2,3,4
- h) PL KEY 1,2,3,4
- i) Zynq XC7Z010 + DRAM
- j) Ethernet port
- k) USB host port
- l) HDMI port



Contents of majority.xdc file

```
set_property PACKAGE_PIN [ ] [get_ports a]
set_property IOSTANDARD LVCMOS33 [get_ports a]
```

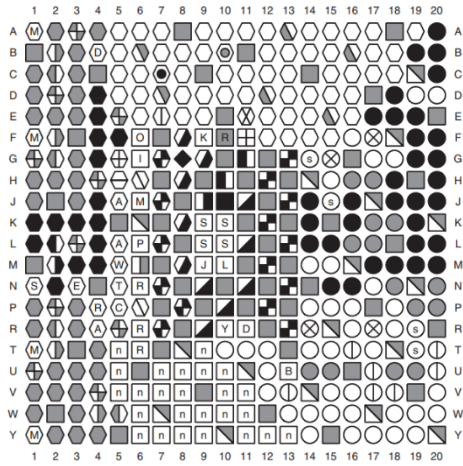
```
set_property PACKAGE_PIN [ ] [get_ports b]
set_property IOSTANDARD LVCMOS33 [get_ports b]
```

```
set_property PACKAGE_PIN [ ] [get_ports c]
set_property IOSTANDARD LVCMOS33 [get_ports c]
```

```
set_property PACKAGE_PIN [ ] [get_ports f]
set_property IOSTANDARD LVCMOS33 [get_ports f]
```



XC7Z010 pinout



CLB architecture

